



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Fumitoshi YAMAMOTO, et al.

Serial No.: 10/642,214

Filed: August 18, 2003

For: SEMICONDUCTOR DEVICE WITH SURGE PROTECTION CIRCUIT

: Customer Number: 20277
: Confirmation Number: 2238
: Group Art Unit: 2836
: Examiner:
:

REQUEST FOR CORRECTED FILING RECEIPT

Mail Stop RQOFR
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is a copy of the Filing Receipt received from the U.S. Patent and Trademark Office in the above-referenced application. It is noted that the Second Assignee listed in the Assignment For Published Patent Application section, is incorrect. Attached is a copy of the Assignment, which evidences that the second Assignee should read **KYOEI SANGYO CO., LTD.** It is requested that a corrected filing receipt be issued.

Respectfully submitted,

MCDERMOTT, WILL & EMERY


Gene Z. Rubinson
Registration No. 33,351

600 13th Street, N.W.
Washington, DC 20005-3096
(202) 756-8000 GZR:gav
Facsimile: (202) 756-8087
Date: January 15, 2004

COPYUNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

APPL NO.	FILING OR 371 (G) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/842,214	08/18/2003	2836	750	67161-081	13	9	3

McDermott, Will & Emery
 600 13th Street, N.W.
 Washington, DC 20005-3096

RECEIVED
 NOV 24 2003
 MW&E

CONFIRMATION NO. 2238

FILING RECEIPT



"OC000000011244882"

Date Mailed: 11/12/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Fumitoshi Yamamoto, Hyogo, JAPAN;
 Yasufumi Murai, Tokyo, JAPAN;
 Keiichi Furuya, Hyogo, JAPAN;

Assignment For Published Patent Application

RENESAS TECHNOLOGY CORP.;
 KYOEI TECHNOLOGY CO., LTD.;
 SAN GYD

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2002-368456 (P) 12/19/2002

If Required, Foreign Filing License Granted: 11/11/2003

Projected Publication Date: 06/24/2004

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor device with surge protection circuit